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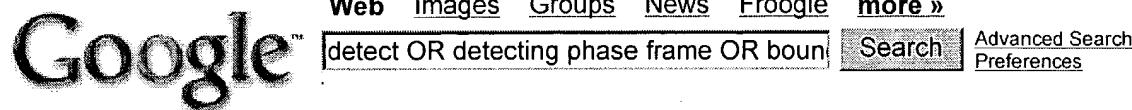
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**Web** Results 1 - 10 of about 5,790 for [detect OR detecting phase frame OR boundary "ECC"](#). (0.57 seconds)

### [Digital Audio Broadcasting](#)

... flash converters with <250picosecond windows, one to **detect** each **phase** ... signal to lock on to the **phase** clock of ... **ECC frame** size is 16 kilobits (8 kilosymbols, 8 ...

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### [CD Freaks.com - Why DVD+R\(W\) is superior to DVD-R\(W\) \[1/1\]](#)

... period lasts 32T, which makes it much easier to **detect**. ... the pre-pits more error prone than **phase** modulation, but ... which gives at least one sync **frame** less to ...

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... High Speed Search User Data Area Previous Group written Total Group written Previous Group read Total Group read Total Rewritten **frame** Total 3rd **ECC** count Load ...

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### [\[PDF\] Microsoft PowerPoint - PCI-SIG PCI-X 2.0 Presentation DRAFT H.ppt](#)

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... Response **Phase** Bus Transaction Initiator Termination TRDY# IRDY# **FRAME#** BUS CMD C ... bit correction on all phases Supports nibble **detect** and **phase** error **detect** ...

[www.pcisig.com/specifications/pcix\\_20/pci\\_x\\_2dot0\\_presentation.pdf](http://www pcisig com/specifications/ pcix_20/pci_x_2dot0_presentation pdf) - [Similar pages](#)

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... The mean screen luminance was 100 cd/m<sup>2</sup> and the **frame** rate was ... FIGURE 3. Comparison of the contrast needed to **detect** and to discriminate the **phase** (0° vs ...

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... SDAT 32 26 SBCK S0S1 EFM **PHASE** DETECTOR 23BIT ... SUBCODE-Q REGISTER DIGITAL PLL **FRAME** SYNC DETECTOR ... MODE SELECTOR DIGITAL OUTPUT 16K SRAM **ECC** INTERPOLATOR DIGITAL ...

[www.samsung.com/.../SystemLSI/DigitalMedia/ OpticalASSP/OpticalPlayer/CDP/S5L9286F/s5l9286f01.pdf](http://www samsung com/.../SystemLSI/DigitalMedia/ OpticalASSP/OpticalPlayer/CDP/S5L9286F/s5l9286f01 pdf) - [Similar pages](#)

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... all stations going to a backoff **phase** before transmission ... In order to **detect** a collision, the entire ... **Frame** Space) • EIFS (Extended Inter **Frame** Space) SIFS and ...

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### [\[PDF\] 15-441 Computer Networking Links From Signals to Packets Link ...](#)

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... On means 1, Off means 0 • Similar: frequency or **phase** modulation ... byte stuffing • Encoding • Add implied 0 to end of **frame** • Each 0 ... Can **detect** all burst ...

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... the Provide process measurements for **detecting** LOCA's ... entire To maintain the pressure **boundary** integrity during ... time under 2 under 2--phase flow conditions ...  
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... on the EFMP **frame** boundaries by **detecting** unique sync ... The digital defect **detect** block allows the loops and ... all digital implementation of a **phase** locked-loop ...

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